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FIG. 1A

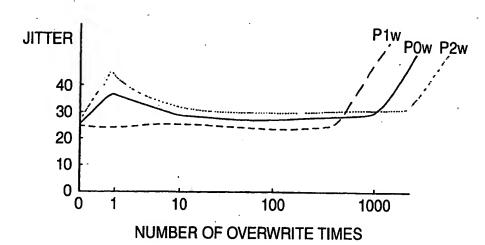
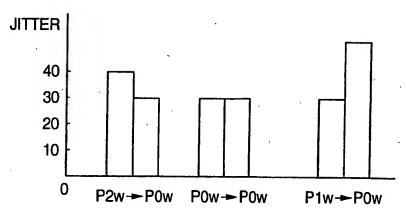


FIG. 1B



P2w < P0w < P1w

P0w: OPTIMUM RECORDING POWER

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FIG. 2A

RECORDING POWER P2w

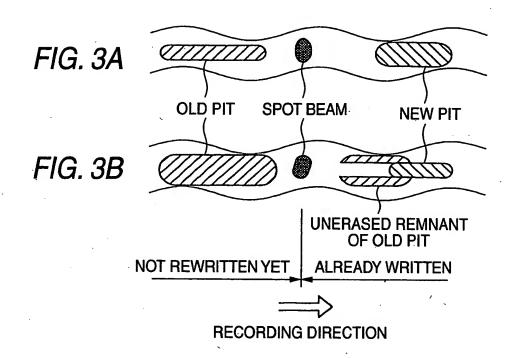
FIG. 2B

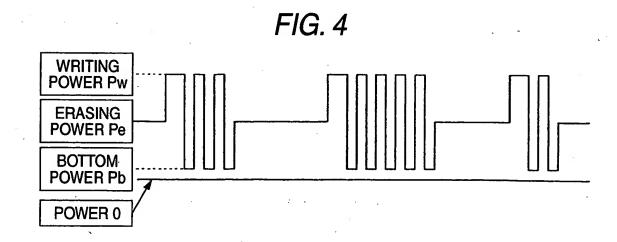
RECORDING POWER POW

FIG. 2C

RECORDING POWER P1w

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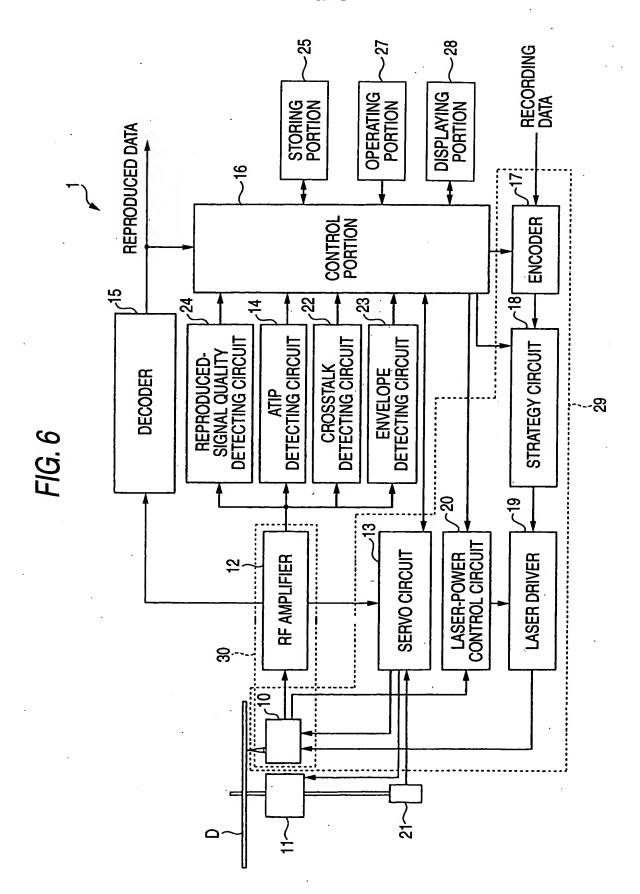


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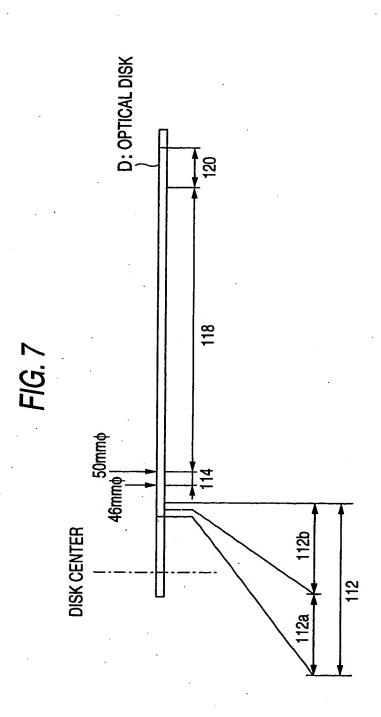
FIG. 5A	SPOT SHAPE	FORMED SPOT
	SPOT POCE DIRECTI	EEDING
FIG. 5B	SPOT POCE DIRECTI	
FIG. 5C	SPOT POCE DIRECTI	
FIG. 5D	SPOT POCE	

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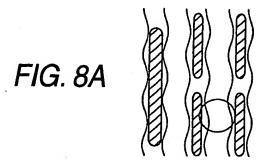




FIG. 9

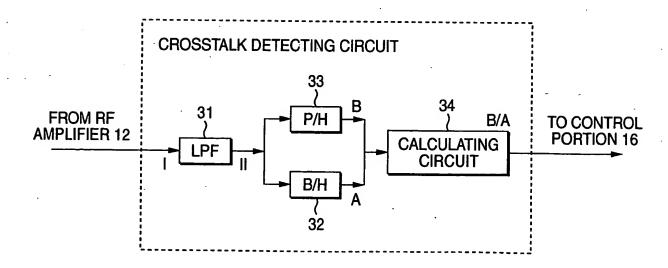


FIG. 10A

SIGNAL I

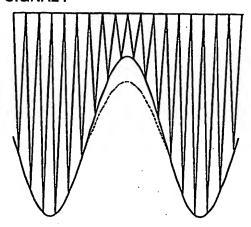
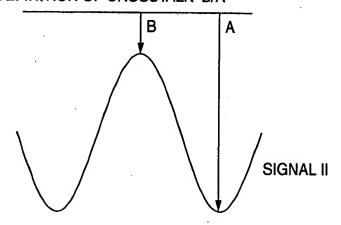


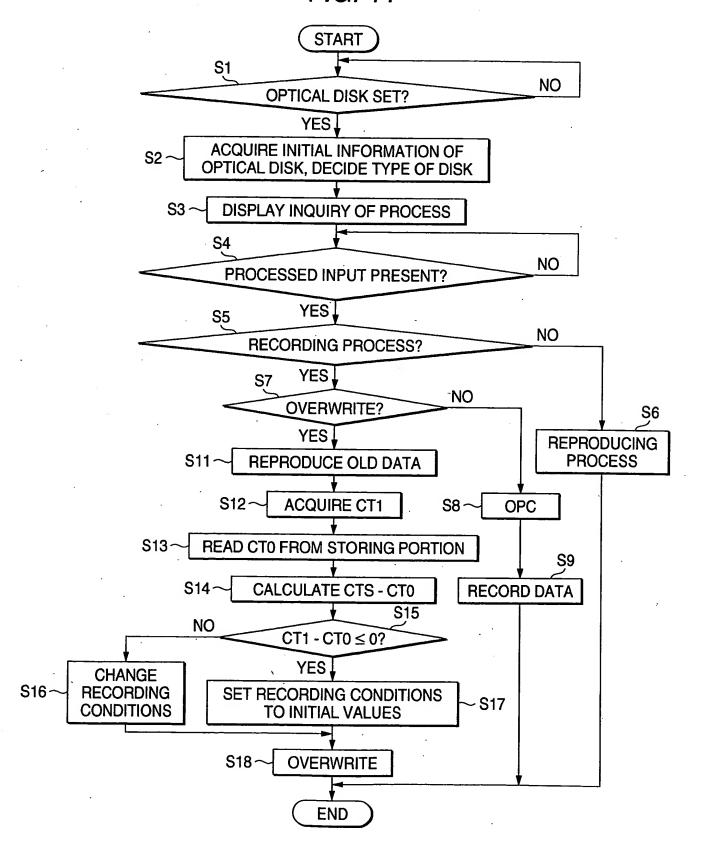
FIG. 10B

DEFINITION OF CROSSTALK B/A

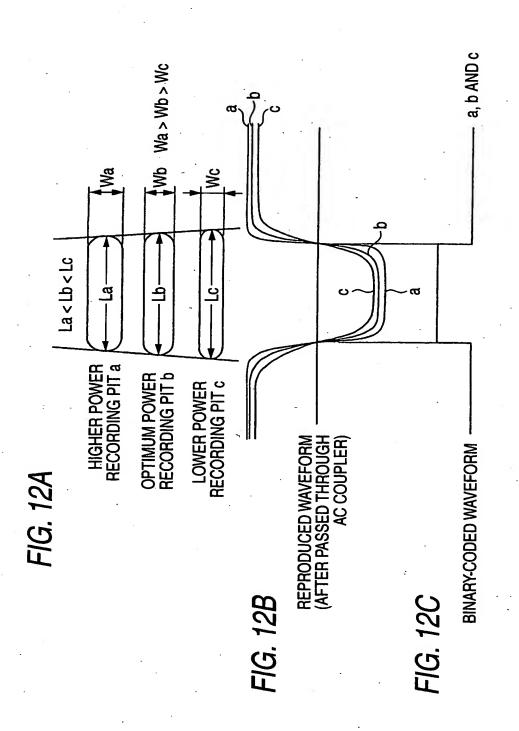


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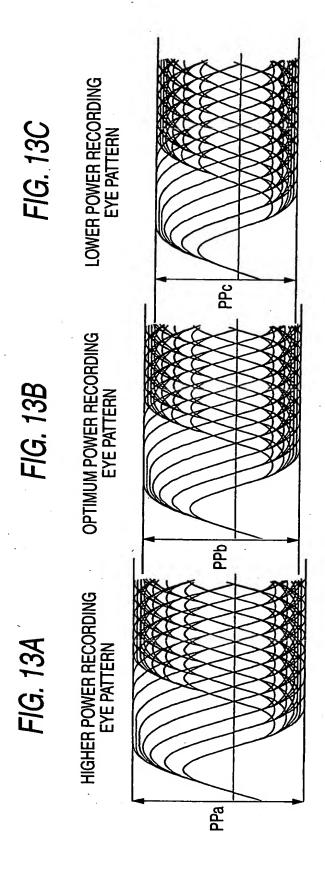
FIG. 11



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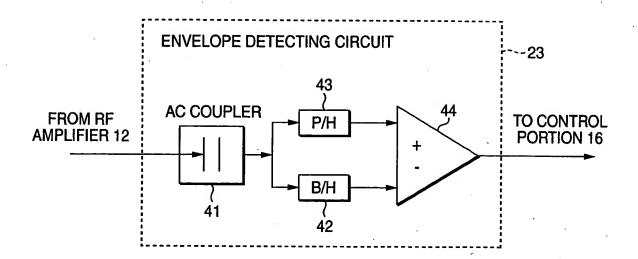
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FIG. 14



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